



孕龍科技股份有限公司
ZeroPlus Technology Co., Ltd.

SPECIFICATION

MODEL: B09016-LAP-SCCB-M

PART NO: _____

VERSION: V1.01

Approver		Check	Design
GM	PM		

Customer Confirm

* Please fax the file to
ZeroPlus Technology after
signing.

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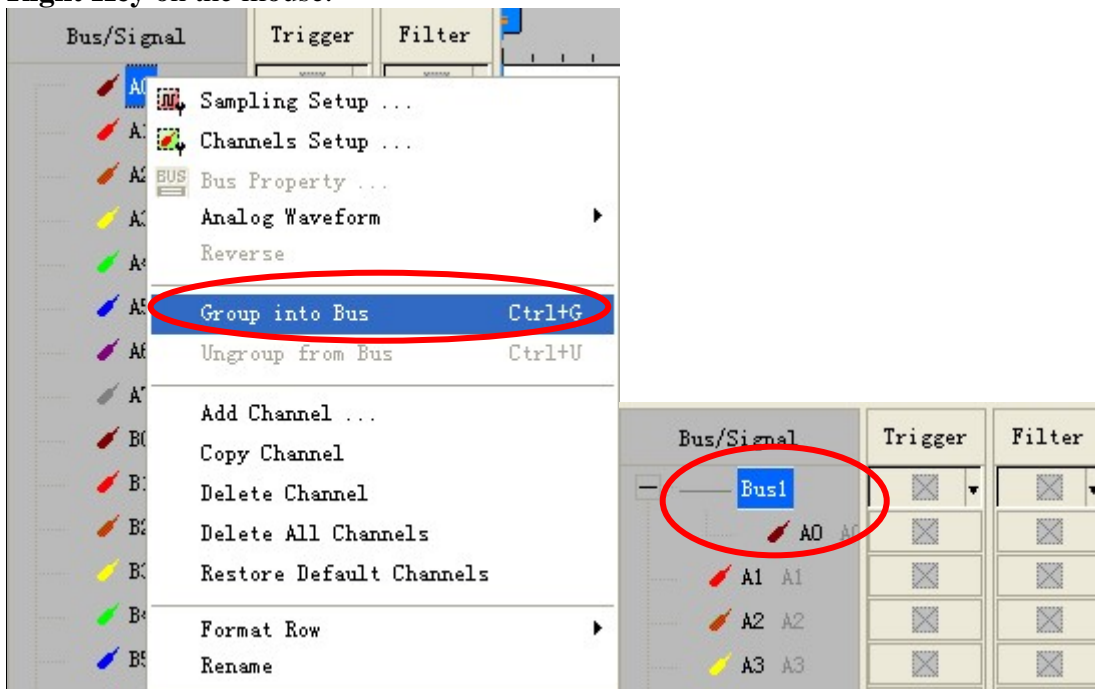
1 Software Register

Please register the software as the following steps:

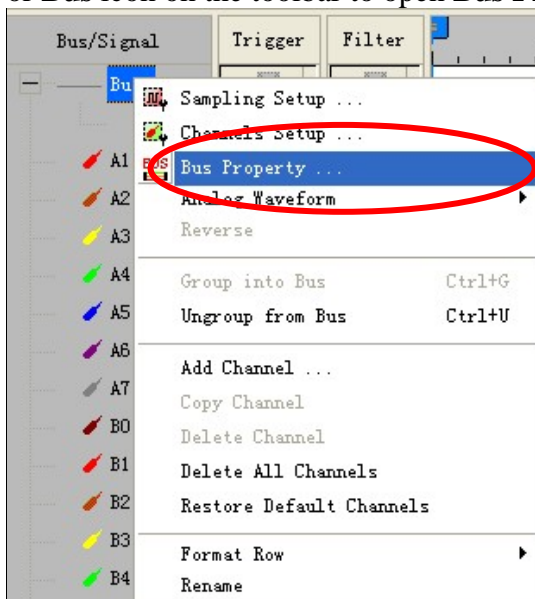
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key** on the mouse.

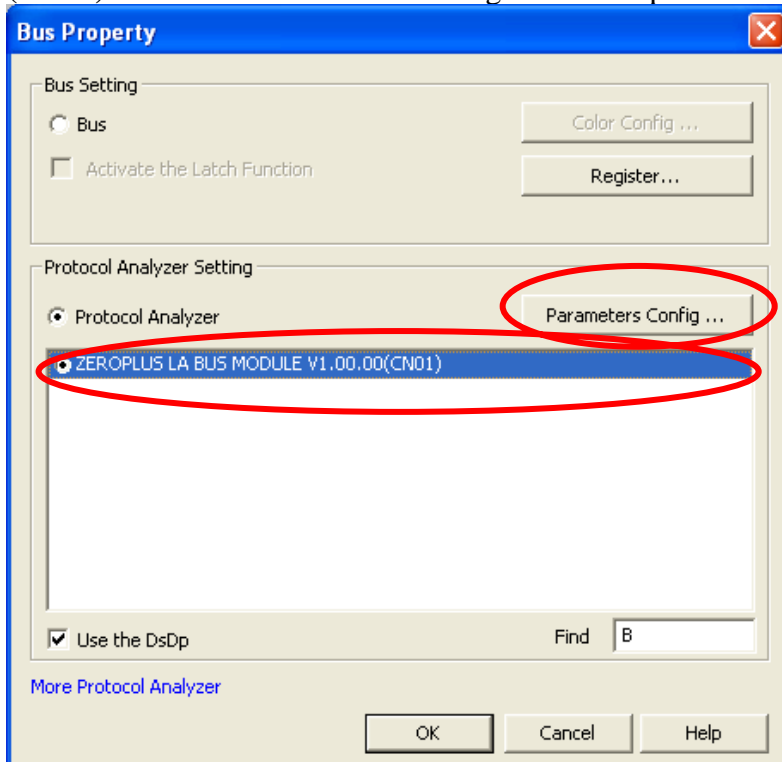


STEP 2. Select **Bus 1**, then press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.

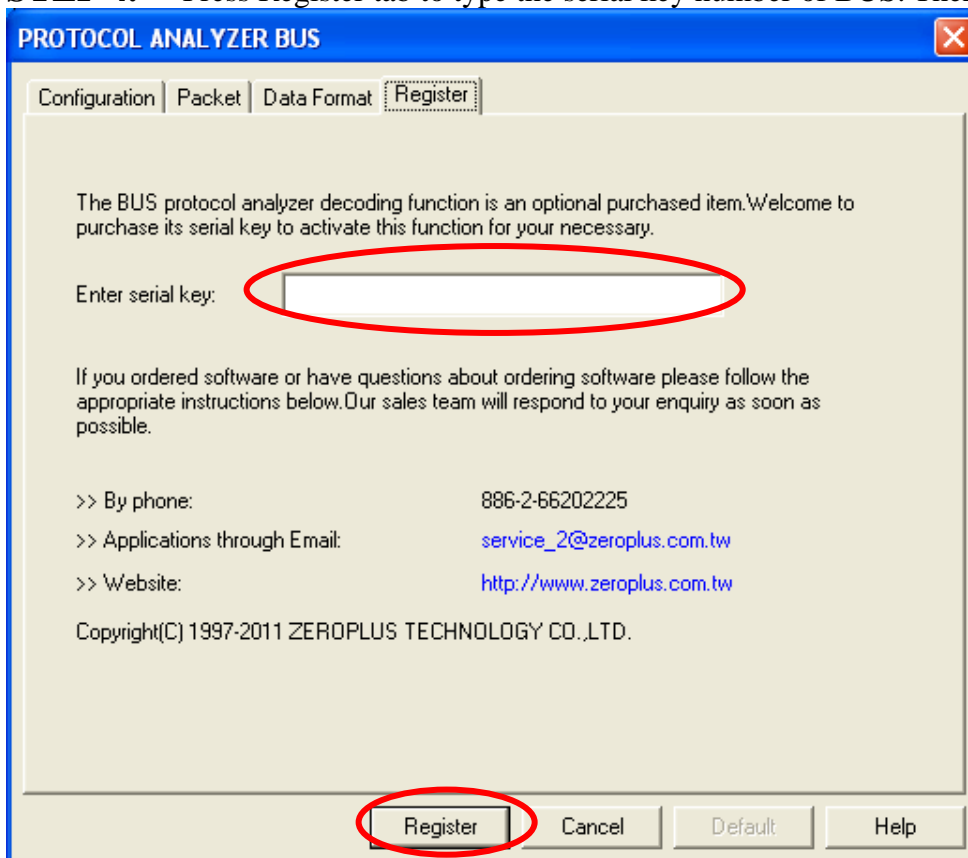




STEP 3. Select the Protocol Analyzer, and then choose **ZEROPLUS LA BUS MODULE V1.00.00 (CN01)**. Next click Parameters Configuration to open Protocol Analyzer Bus dialog box.

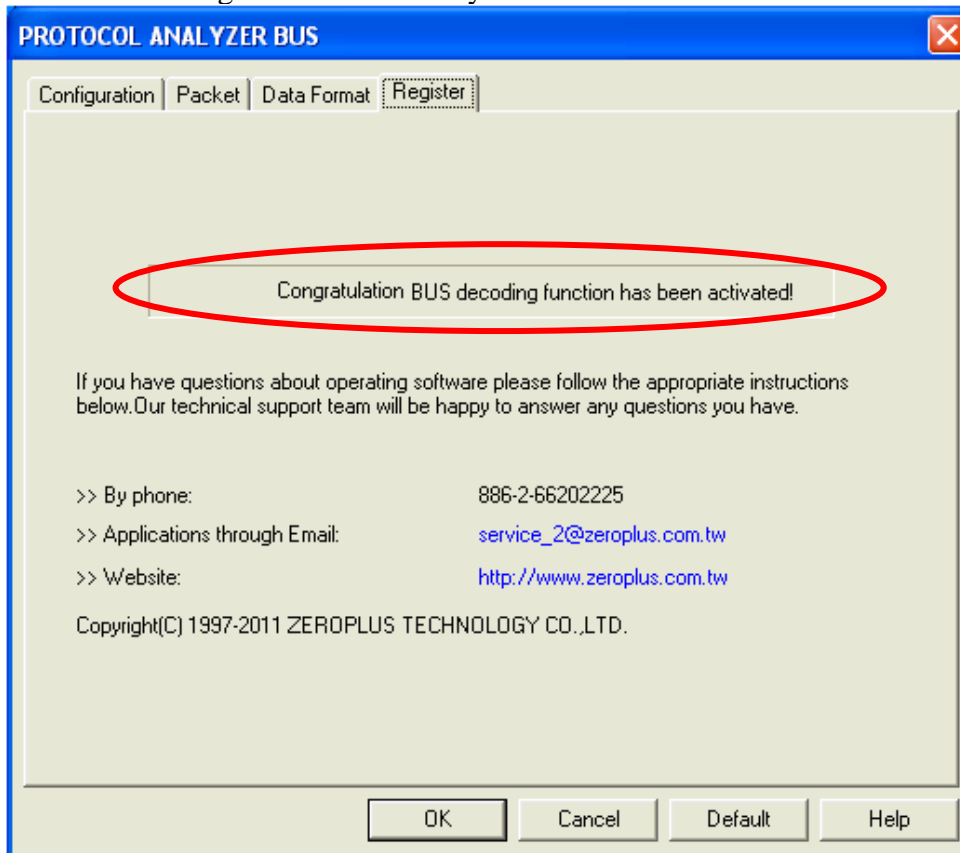


STEP 4. Press Register tab to type the serial key number of BUS. Then press Register.





STEP 5. After pressing the Register button, following dialog box will appear, it denotes that the BUS has been registered successfully.

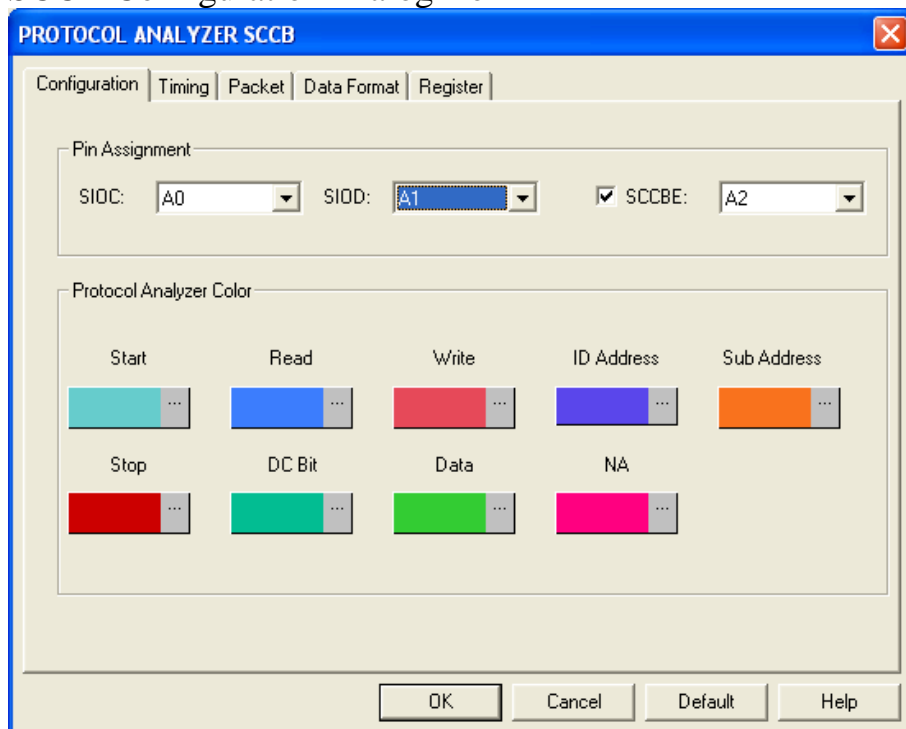




2 User Interface

In the configuration, please refer to the below images to select options of setting SCCB module.

SCCB Configuration Dialog Box



Pin Assignment:

SIOC: It is the Clock Signal Channel.

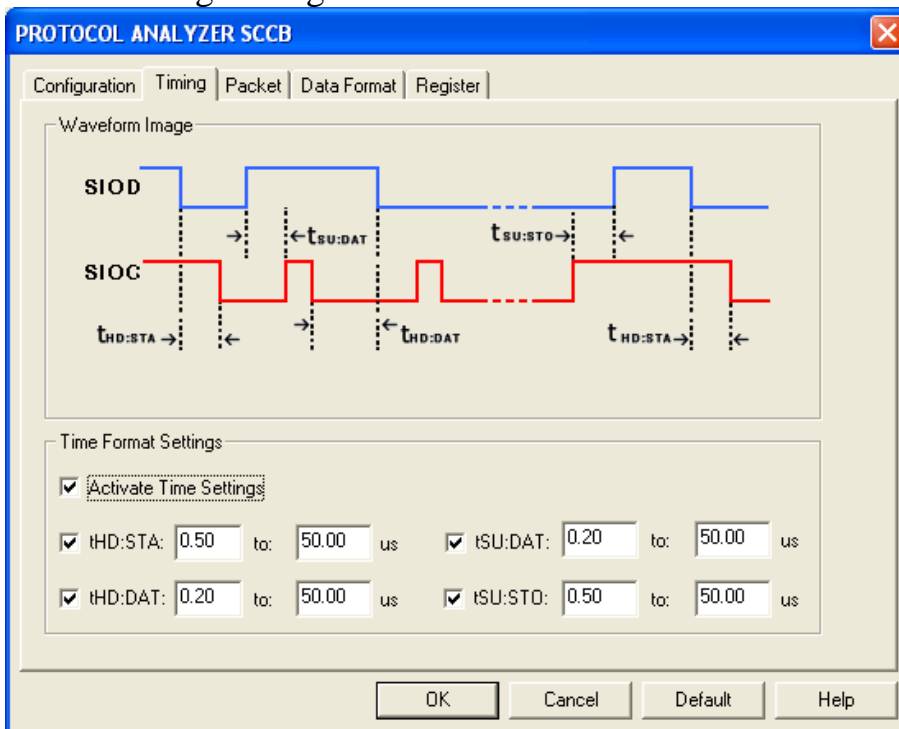
SIOD: It is the Data Signal Channel.

SCCBE: It is the Chip Select Control Channel. It is effective in the Low Level. The default is to be selected. When not selecting this item, SCCBE, it means that the SCCBE is always in the Low Level.

Protocol Analyzer Color: The Protocol Analyzer Color can be varied by users.

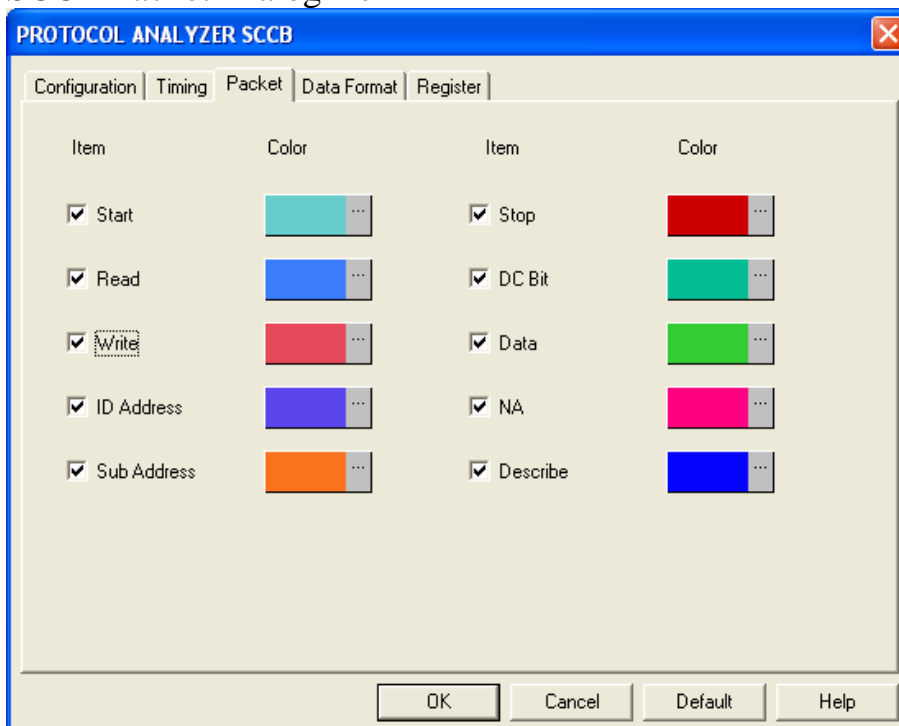


SCCB Timing Dialog Box



START Time Interval, $t_{HD: STA}$, is from the Falling Edge of the SIOD to the Falling Edge of the SIOC; END Time Interval, $t_{SU: STO}$, is from the Rising Edge of the SIOC to the Rising Edge of the SIOD; the Time Sequence of data can be divided into two parts. $t_{SU: DAT}$ is the Time Interval which is from the Rising Edge or Falling Edge of the SIOD to the Rising Edge of the SIOC; $t_{HD: DAT}$ is the Time Interval which is from the Falling Edge of the SIOC to the Rising Edge or Falling Edge of the SIOD.

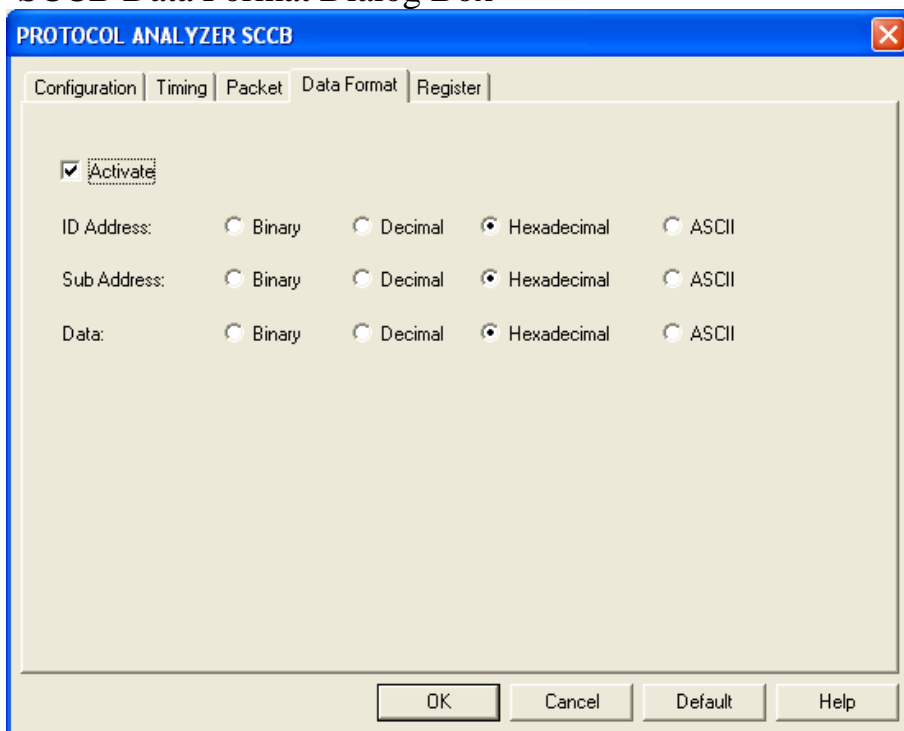
SCCB Packet Dialog Box



In the Packet part, users can set the items and colors as users' requirements.

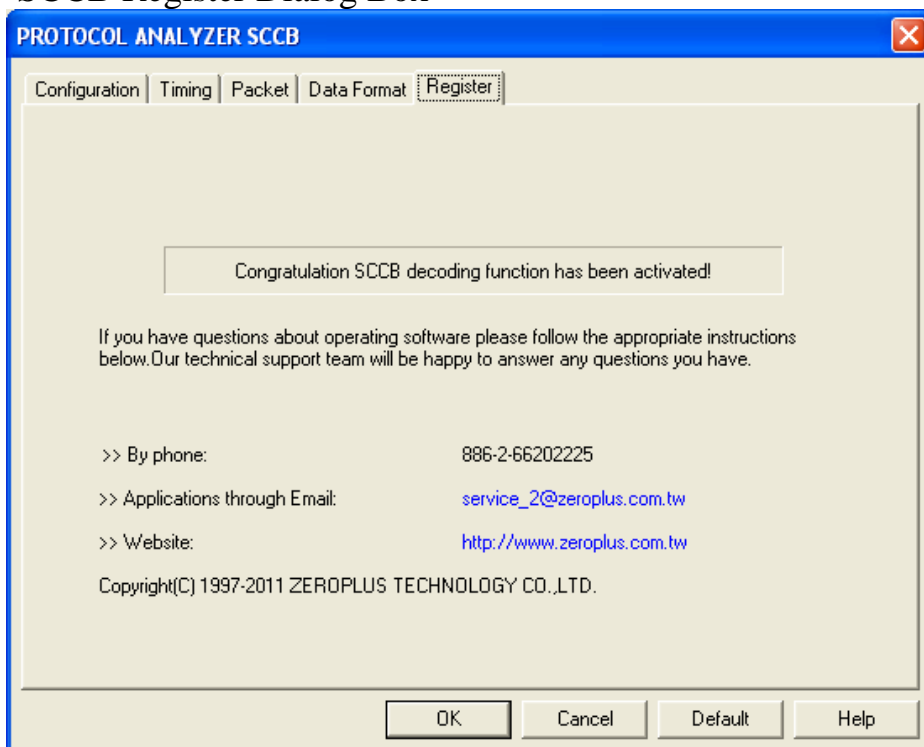


SCCB Data Format Dialog Box



Users can set the Data Format of the ID Address, Sub Address and Data as their requirements. When selecting the option, Activate, the data formats are decided by the settings in the Protocol Analyzer; when not selecting the option, Activate, the data formats are decided by the settings in the main program.

SCCB Register Dialog Box

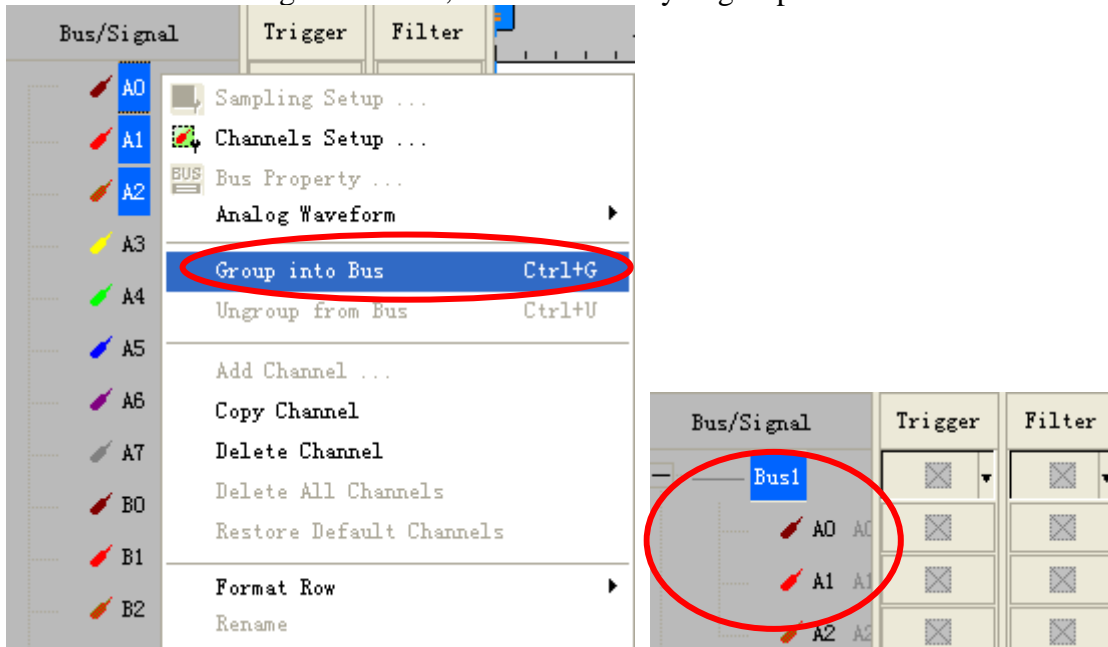


There is written ZeroPlus company information. If you have any questions about software operations, you can contact ZeroPlus by Telephone or Email.

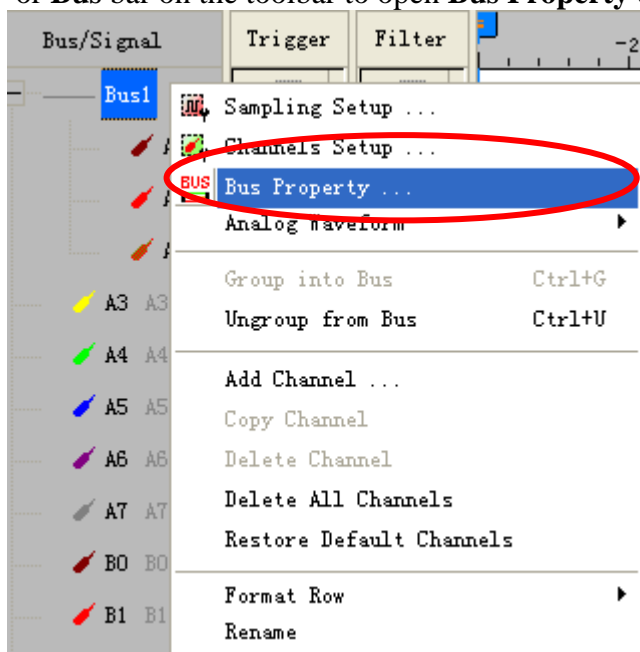


3 Operating Instructions

STEP 1. Group A0-A2 into **Bus1** by pressing the **Right Key** on the mouse. SCCB needs three channels to decode signals at least, so it is necessary to group three or more channels into a Bus.

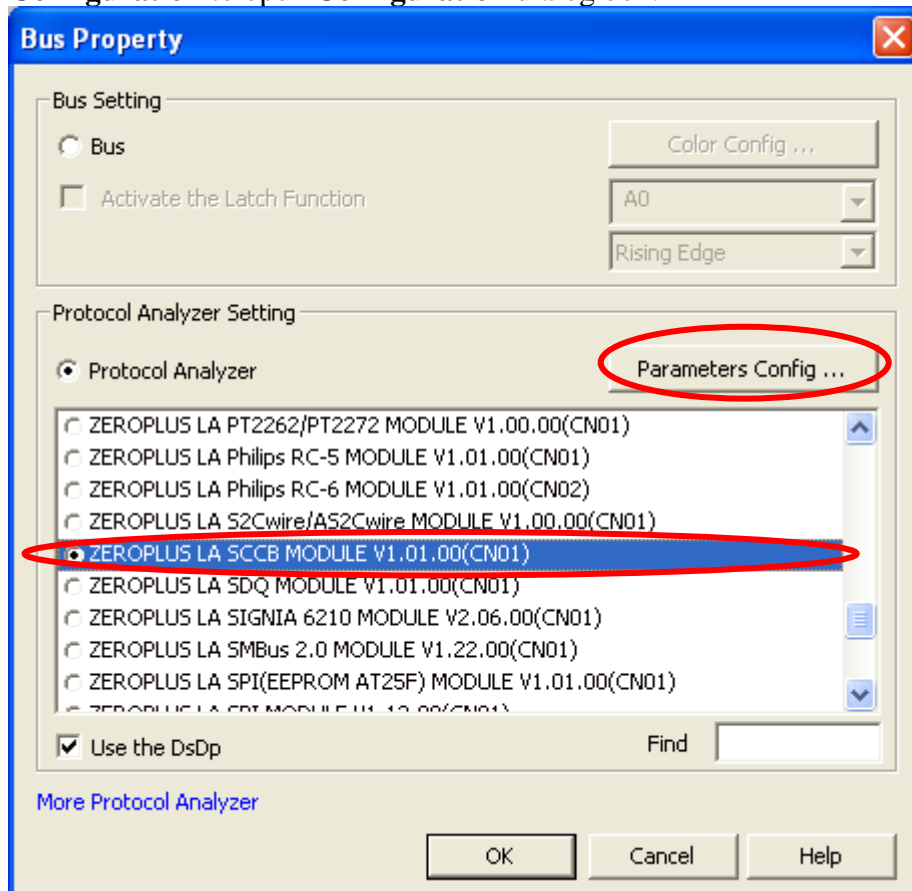


STEP 2. Select **Bus1**, and press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** bar on the toolbar to open **Bus Property** dialog box.

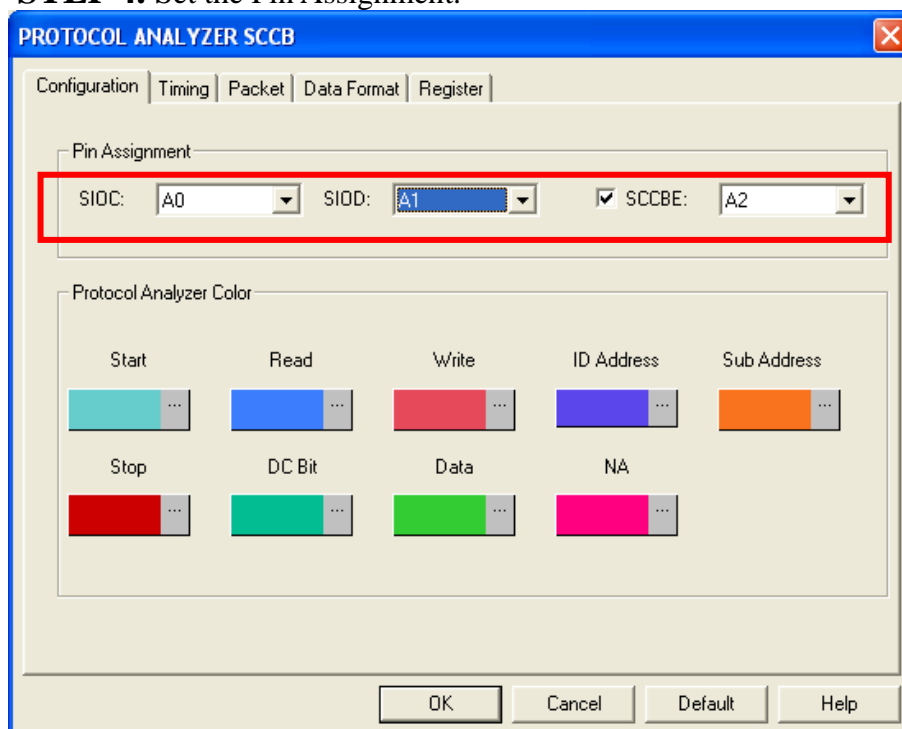


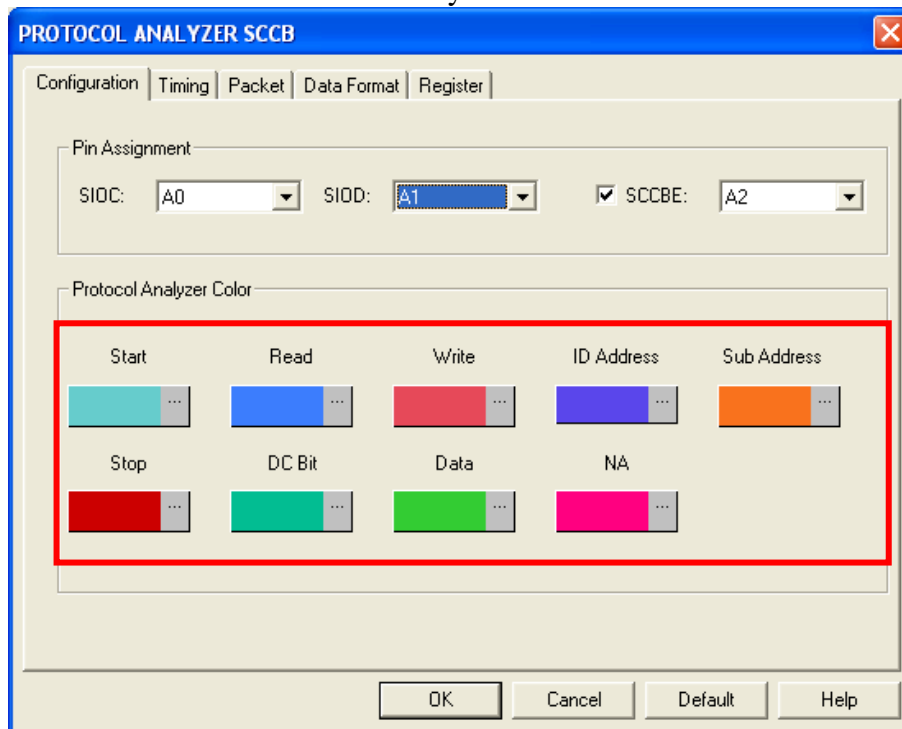


STEP 3. For Protocol Analyzer SCCB Parameters Configuration, select Protocol Analyzer, and then choose **ZEROPLUS LA SCCB MODULE V1.01.00(CN01)**. Next click **Parameters Configuration** to open **Configuration** dialog box.



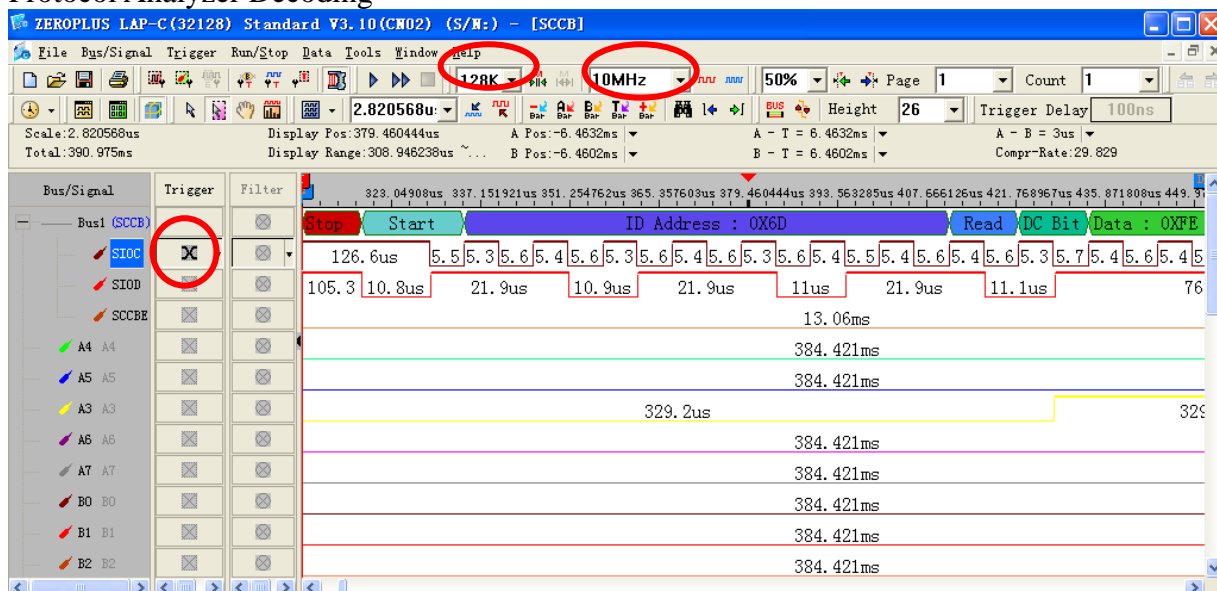
STEP 4. Set the Pin Assignment.





STEP 6. Following pictures show the completion of the protocol analyzer decoding and the packet list. The trigger condition is set as Either Edge; the memory depth is 128K; the sampling frequency is 10MHZ. (the sampling frequency should be more than eight times higher than the signal to be tested)

Protocol Analyzer Decoding





Packet List

